



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Takashi KURIHARA, et al.

Application No.: 10/823,649

ATTENTION

Confirmation No.: 4917

CERTIFICATE OF CORRECTION

Filed: April 14, 2004

BRANCH

U.S. Patent No.: 7,454,724

Issued: November 18, 2008

For: METHOD AND APPARATUS DISTRIBUTION POWER SUPPLY PAD OF SEMICONDUCTOR
INTEGRATED CIRCUIT (as amended)**REQUEST FOR CERTIFICATE OF CORRECTION**

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

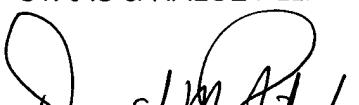
Sir:

Patentees respectfully request that a Certificate of Correction be issued in the subject patent, pursuant to 35 USC § 254 and 37 CFR § 1.322, to correct the errors shown on the attached Form PTO-1050. A check in the amount of \$100 to cover the cost of the Certificate is attached.

One or more of the errors shown on the attached Form PTO-1050 is or are within the responsibility of the applicant; each thereof is of a clerical or typographical nature or of minor character and occurred in good faith, and the correction thereof is consistent with the prosecution record.

Respectfully submitted,

STAAS & HALSEY LLP

By: 

David M. Pitcher
Registration No. 25,908

*Certificate
JAN 26 2009
of Correction*

Date: January 12 2009

1201 New York Ave, N.W., 7th Floor
Washington, D.C. 20005
Telephone: (202) 434-1500
Facsimile: (202) 434-1501

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO : 7,454,724

DATED : November 18, 2008

INVENTOR(S) : Takashi KURIHARA, et al.

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

First Page, Column 1 (Title), Line 2, change "SUPPLY" to --SUPPLY--.

Column 1, Line 2, change "SUPPLY" to --SUPPLY--.

Column 21, Line 40, change "processor;" to --processor:--.

Column 22, Line 47, change "IC" to --IO--.

MAILING ADDRESS OF SENDER:
STAAS & HALSEY LLP
1201 New York Avenue, N.W.
7th Floor
Washington, DC 20005

PATENT NO. 7,454,724

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